Hierarchical Planning in the IPC

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The hierarchical planning (sub-)community

has no common description language

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- has no consensus on features that must be supported
- has no standard benchmarks to compare planners
- often cannot compare planners for theoretical reasons
- is generally lacking focus

General HTN planning

- FAPE a temporal HTN planner with strong pruning techniques
- PANDA a plan-space planner using heuristic search
- PANDApro a progression-based planning system using heuristic search
- HTN2STRIPS a planner translating HTN planning problems into a sequence of classical planning problems
- partSAT a planner based on a translation into propositional logic

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Totally-Ordered HTN planning

- GTOHP a planner based on intelligent grounding and blind search
- totSAT planners that translates (totally-ordered) HTN planning problems into propositional logic
- HTN2ASP a planner that translates (totally-ordered) HTN planning problems into answer set programming

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- Maybe restrict allowed changes for organisers?

Tracks

- Optimal
- Satisficing
- Agile

- General HTNs without state constraints
- General HTNs with state constraints (# competitors?)
- Totally-Ordered HTNs

Setting

We propose to use standard IPC setting

- 1 CPU core
- 8 GB of RAM
- 30 minutes

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For Domains we propose a mix of

- Current benchmark domains (as far as compatible)
- Community submissions
- BYOB = Bring-Your-Own-Benchmark
 - Each submitted planner must provide a domain with 20 (?) instances
 - The submitting planner must solve at most half of these instances
 - Used by SAT-Races 2017 and 2018

Plans produced by the planner must be verified.

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- \mathbb{P} when planners output the decomposition (i.e. derivation)
- Two possible approaches
 - 1 Use two existing HTN plan verifier (Barak et al. & Behnke et al.)
 - Require planners to output decomposition using a yet to be defined standard format

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 Not all planners backtrack, it is highly algorithm-specific
 - Depth of search Not well defined
 - Number of methods All planners must use the same problem

Timetable

May – July 2019	Agreeing on a common input language
July 2019	Announcement of the track
	Call for domains
	Call for expression of interest
October 2019	Registration deadline
November 2019	Demo problems provided
January 2020	Submission of preliminary planner versions
February 2020	Domain submission deadline
April 2020	Final planner submission deadline
May 2020	Paper submission deadline
May 2020	Contest run
June 2020	Presentation of the results at ICAPS 2020